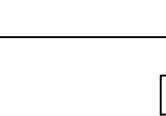
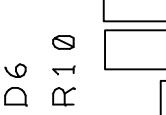
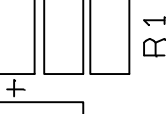
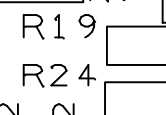
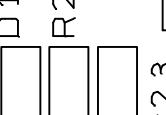
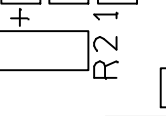
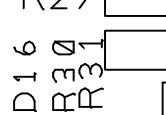
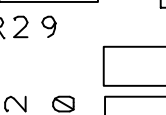
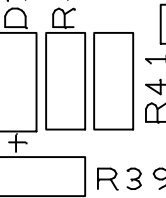
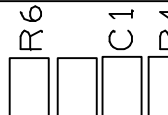
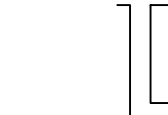
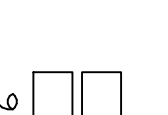
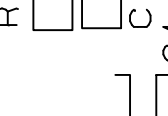
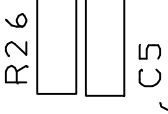
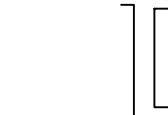
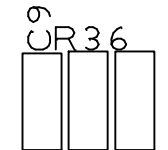
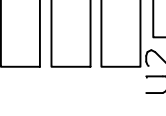
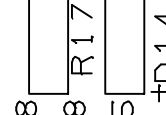
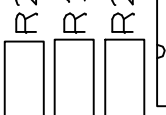
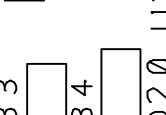
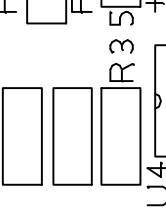
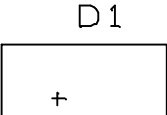
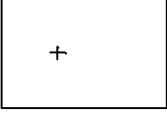
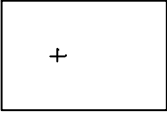
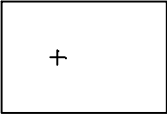
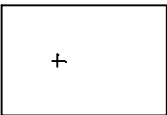
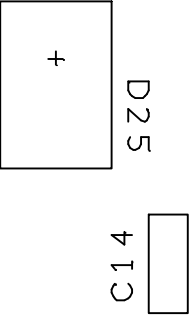


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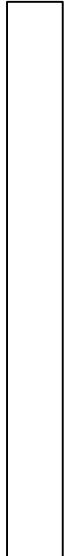
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DCC TOTI
V1.01

O/C COL. OUTPUTS

J 2



COM -

$$D5 +$$

$$D2 +$$

$$D8 +$$

$$D11 +$$

$$D12 +$$

$$D18 +$$

$$D31 +$$

$$D34 +$$